		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		09/944,716	RAVAL ET AL.
		Examiner	Art Unit	Page 2 of 2
		Melvin H Pollack	2145	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,578,174	06-2003	Zizzo, Claudio	716/1
	B	US-6,334,207	12-2001	Joly et al.	716/17
	C	US-6,826,717	11-2004	Draper et al.	714/39
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		09/944,716	RAVAL ET AL.
		Examiner	Art Unit	
		Melvin H Pollack	2145	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,457,152	09-2002	Paley et al.	714/738
	B	US-6,832,184	12-2004	Bleier et al.	703/23
	C	US-6,389,379	05-2002	Lin et al.	703/14
	D	US-6,606,588	08-2003	Schaumont et al.	703/15
	E	US-6,446,121	09-2002	Shah et al.	709/224
	F	US-6,473,803	10-2002	Stern et al.	709/238
	G	US-6,360,353	03-2002	Pember et al.	716/4
	H	US-6,839,884	01-2005	Narain et al.	716/5
	I	US-6,470,482	10-2002	Rostoker et al.	716/6
	J	US-5,881,267	03-1999	Dearth et al.	703/27
	K	US-5,600,579	02-1997	Steinmetz, Jr., William C.	703/13
	L	US-5,910,897	06-1999	Dangelo et al.	716/19
	M	US-6,438,739	08-2002	Yamada, Akihisa	716/18

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stevens, W. Richard. "TCP/IP Illustrated, Volume 1: The Protocols," Addison-Wesley, 1994.
	V	Calvez, J.P. et al. "Functional-level synthesis with VHDL," European Design Automation Conference, 1993, with EURO-VHDL '93. 20-24 Sept. 1993. Pp. 554-559.
	W	Touba, N. A. "RP-SYN: synthesis of random pattern testable circuits with test point insertion," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, Issue 8, August 1999, Pp. 1202-1213.
	X	Chen, Y. W. "Study on the Prevention of SYN flooding by using Traffic Policing," Network Operations and Management Symposium, 2000, 10-14 April 2000, Pp. 593-604.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.